

## TRANSMITTAL

Electronic Version v1.1

Stylesheet Version v1.1.0

<b>Title of Invention</b>	EXTERNAL VERIFICATION OF PACKAGE PROCESSED LINEWIDTHS AND SPACINGS IN SEMICONDUCTOR PACKAGES
Application Number :	
Date :	
First Named Applicant:	Thomas J. Fleischman
Confirmation Number:	
Attorney Docket Number:	FIS920030167US1

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<b>Documents being submitted:</b>	<b>Files</b>
us-ids	FIS920030167US1-usidst.xml us-ids.dtd us-ids.xsl
us-assignment	FIS920030167US1-usassn.xml us-assignment.xsl us-assignment.dtd FIS9200230167assign1of2.tif FIS9200230167assign2of2.tif
us-request	FIS920030167US1-usrequ.xml us-request.dtd us-request.xsl
us-fee-sheet	FIS920030167US1-usfees.xml us-fee-sheet.xsl us-fee-sheet.dtd
us-declaration	FIS9200230167dec1of2.tif
application-body	fis920030167us1-trans.xml us-application-body.xsl application-body.dtd wipo.ent mathml2.dtd mathml2-qname-1.mod isoamsa.ent isoamsb.ent isoamsc.ent isoamsn.ent isoamso.ent isoamsr.ent isogrk3.ent isomfrk.ent isomopf.ent isomscr.ent isotech.ent isobox.ent isocyr1.ent isocyr2.ent isodia.ent isolat1.ent isolat2.ent isonum.ent isopub.ent mmlextra.ent mmlalias.ent soextblk.dtd FIS92030167US1Drw1.tif FIS92030167US1Drw2.tif FIS92030167US1Drw3.tif FIS92030167US1Drw4.tif FIS92030167US1Drw5.tif FIS92030167US1Drw6.tif

**Comments**

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